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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): O. SUZUKI, et al

Serial No.: 10/722,597

Filed: November 28, 2003

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEREOF

LETTER CLAIMING RIGHT OF PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 14, 2004

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, the applicant(s) hereby
claim(s) the right of priority based on:

Japanese Patent Application No. 2002-346160
Filed: November 28, 2002

A certified copy of said Japanese Patent Application is attached.

Respectfully submitted,

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MK/rr
Attachment